



International Institute of Information Technology Bangalore (A Deemed University)

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Director's Message

The graduate programmes at IITB are among the best in the country, offering education oriented towards producing highly-qualified practitioners and researchers. As an independent institution and a deemed university, IITB collaborates with the IT industry, leading academic institutions abroad as well as India, and eminent scientists and industry leaders to offer students the best possible education. Our students have been well received by the industry, and have been placed with some of the leading companies in the field; in fact, IITB can boast of 100% placement for all its graduates. Some of our Masters students are also pursuing doctoral degrees both in India and abroad, with a view to enter academic / research careers.

Students are encouraged to ask questions, pursue their research interests, and gain membership and respect in the select club of high-achieving IT professionals. The infrastructure and interactions that IITB offers students and faculty makes possible the pursuit of individual and joint projects, industry-sponsored research, and study that is of the highest standards required for global competitiveness.

The institute has a relaxed, friendly atmosphere, but with a constant buzz of excitement due to many well-known visitors including giants from the industry, well-known scientists, and leaders of government from all over the world.

Admission is selective but not restrictive. We are very proud of the achievements of our alumni, and strive to gain good students who will achieve their potential and continue this trend. We thus warmly welcome applicants from different backgrounds who share our vision of excellence.

*S. Sadagopan
Director*

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Genesis

The International Institute of Information Technology Bangalore, a Deemed University, popularly known as IITB, was established in 1999 with a vision to contribute to the IT world by focusing on education and research, entrepreneurship and innovation. The Institute is a registered not-for-profit society funded jointly by the Government of Karnataka and the IT industry.

Since its inception, IITB, with its unique model of education, research, and industry interaction, has grown in stature to become an institution of considerable repute in academic as well as corporate circles. The Institute works in partnership with the corporate sector, while retaining the freedom of an academic institution. It is inspired by other renowned institutions, and also strives to emulate an academic culture that is on par with the best international institutions.

Why IITB

The IT industry in India as a whole is seeing an increasing shift from low-end programming or service-oriented jobs to high-end research and design that competes with the best worldwide. Many young people seeking entry to the IT sector find that they do not have the necessary technical or research background to obtain the types of roles they seek in their careers and must settle for unfulfilling work, and industry as well is frustrated by the lack of quality in the talent pool from which it must create tomorrow's exciting innovations and products. The International Institute of Information Technology-Bangalore offers students a unique environment that provides them with state-of-the-art knowledge in multiple disciplines beyond traditional computer science, covering the wider spectrum of Information Technology, and equips students to provide thought leadership and gain the satisfaction of fulfilling their creative and intellectual potential by becoming full peers to the brightest minds in the world, rather than just providing the low-end labor to implement others' grand visions.

Considering the pervasive nature of IT in all walks of life, IITB encourages and admits students from a wide variety of academic disciplines into the programmes. About half of our students in recent years have been bright young men and women who already have some work experience, but have decided to take a break from their careers to pursue our M.Tech. programme in order to shift their careers on to more stimulating and rewarding paths. Strong interaction with the industry is built into our academic programmes because IITB believes in equipping students to become productive right from the day they enter the industry. For those aspiring for academic goals, IITB provides a research environment as well. Students have the opportunity to get financial aid in the form of scholarships and internships to help meet tuition and other expenses, so a bright student is limited only by his or her own vision and ambition.

Location

Bangalore is often called the Silicon Valley of India. With the pleasant climate, the urban amenities, the rich tradition of technical education, and the highly professional ambience, numerous global and local corporate big-name entities have found a home here. The IITB campus is located in the heart of Electronics City, one of the prestigious IT destinations in Bangalore, with excellent infrastructure, facilities, and services.

The list of companies located in the high-tech area close to the Institute is a virtual Who's Who of the IT business. IITB is situated opposite the Infosys corporate headquarters, and is within easy walking distance of other major companies like HP, Siemens, Wipro, HCL, GE, Tata, and others, giving students and faculty opportunities for productive interaction with the industry.

Campus & Housing

The Institute moved to its present Electronics City campus in 2003. The campus features well-maintained lush green lawns, musical fountains, and a small pond, creating an ideal learning environment to stimulate intellectual and personal growth.

Designed and built in compliance with the highest global standards, the teaching and learning infrastructure features the most advanced elements of contemporary academic tools. With addition of a new floor to the main academic block, the institute now has over 120,000 square feet of air-conditioned space, uninterrupted power supply, and a well-crafted interior, the Institute offers a world-class environment for students and faculty.

All the classrooms are "smart," with high-speed data networks and large projection systems for audio and video. The well-designed main classroom comfortably seats in excess of 150 students. Video conferencing capabilities are built in using state-of-the-art audio-visual equipment. They include electronic smart boards, location-sensing microphones, and multiple LCD projectors, thus enabling an enriching learning experience.

All the academic programmes are residential in nature. Separate hostel facility is available for men and women. Students get individual rooms. Meals are available at the Food Court located in a separate building (the food that our students have is the same food that is also had by faculty and staff, and offered to visitors including distinguished dignitaries, thus ensuring high quality). A separate cafeteria is available for small snacks during working hours.

For recreation inside the campus, indoor games (carroms, chess and table tennis) and a limited number of outdoor games (cricket and basket ball) are available. All the hostel blocks are equipped with a reading room and a music/meditation hall and gymnasium facility.

Programmes Offered

The Institute is an exclusive graduate school that offers programmes that lead to post-graduate degrees. The graduate study programmes offered lead to the Integrated Master of Technology (Integrated M.Tech.), Master of Technology (M.Tech.), the Master of Science (M.S.) by Research, and the Doctor of Philosophy (Ph.D.) degrees in information technology. These programmes offer training that is comparable to the best anywhere, and cater to different needs.

Integrated M.Tech.

Integrated M.Tech. Programme is a five-year full time dual degree programme intended for students who have passed or are appearing for Senior Secondary (Plus Two) examination with Mathematics or equivalent examinations. . At the end of five years of the programme, successful students get both B.Tech. and M.Tech. degree in IT. Please see the eligibility criteria for further details

M.Tech

The M.Tech. degree is intended to provide education for students who wish to work in the IT industry as practitioners. It is awarded upon successful completion of a 4-semester broad-based academic programme in IT, going beyond traditional computer science. Apart from imparting technical knowledge, the programme teaches managerial and other life skills that are essential for a successful career in today's competitive IT industry. All students enrolled in the M.Tech. programme are residential, full-time students. The regular M.Tech. programme is primarily intended for those who have a bachelors degree in engineering. Please see the eligibility criterial for further details.

NB: There is no part-time M.Tech programme for working professionals.

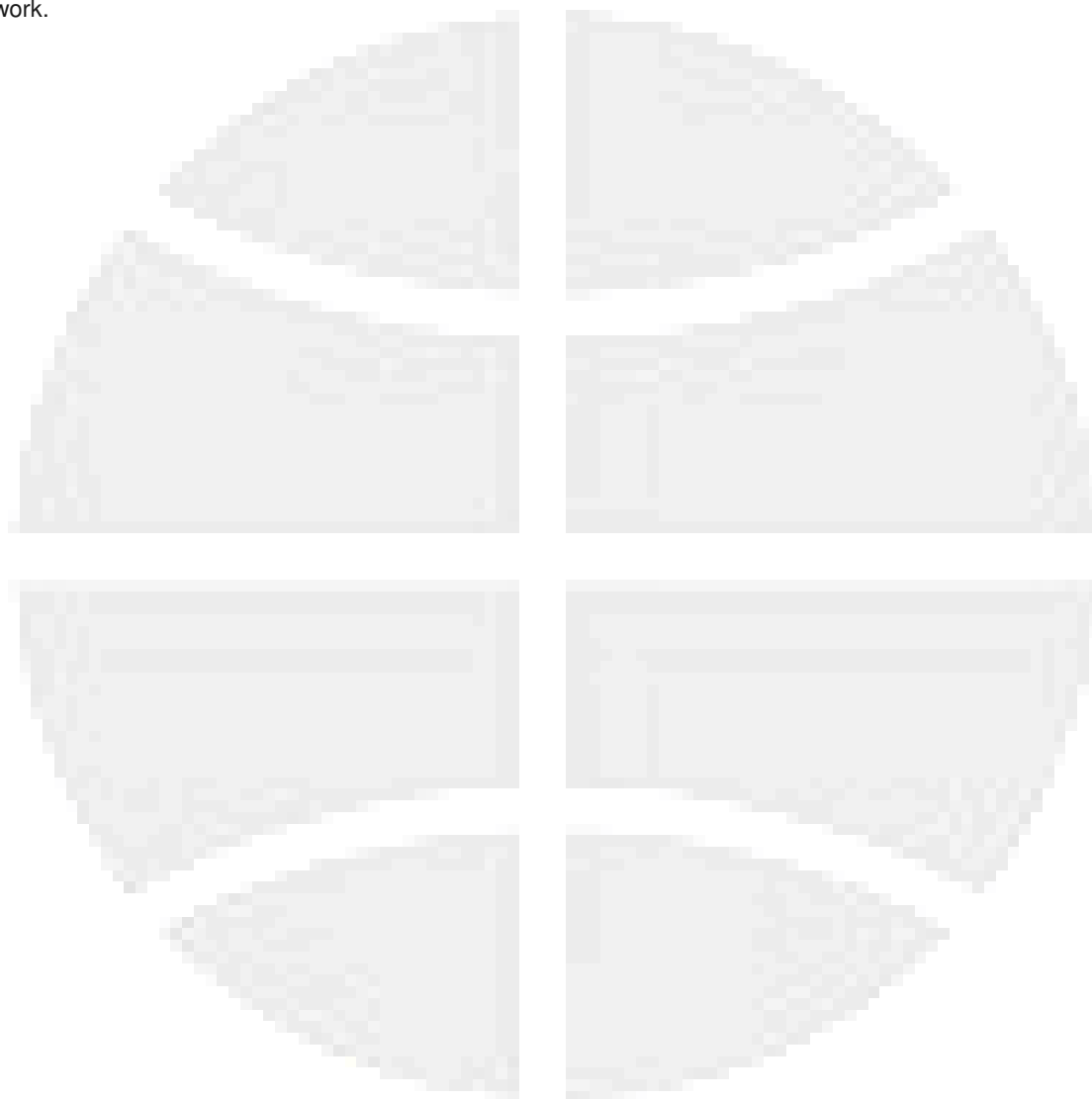
M.S. by Research

The Institute offers two research programmes for students who are interested in carrying out quality research in the chosen field of study.

The M.S. by Research degree is intended for mature students who wish to learn and perform research in a supportive academic environment. It is awarded upon successful completion of a graduate-level research programme, usually lasting four semesters. Beyond the satisfaction of a relatively small number of coursework requirements, the major focus of the programme is on developing research skills, leading to the completion of a Master's thesis describing significant original results. Most M.S. by Research students are working professionals (some already working as research scientists in reputed organizations or government bodies), who are sponsored by their employers.

Ph.D.

The Ph.D. is the terminal, research-oriented degree in the subject, intended to prepare students for research, teaching, and scholarly careers in academic settings or research laboratories. It connotes a superior comprehension of the field and a high aptitude for research, and is awarded upon completion of a programme that takes from 3 to 5 years. Beyond the satisfaction of a relatively small number of coursework requirements, the major focus is on carrying out a significant body of original research and the writing and defense of a doctoral dissertation describing this work.



Course Structure

Fundamental to Information Technology is the integration of different technologies and the integration of technologies into organizations. The uniqueness of the IITB curriculum is that it makes for a broad-based programme going beyond traditional computer science, and enables students to function well in the industry, while keeping the rigorous learning expected of a classical programme.

Integrated M.Tech. Course Structure

The period of the Integrated M.Tech. programme will be a minimum of five years. The total number of credits required to fulfill requirements of the programme is 204. The course curriculum includes courses that are mandatory for all the students (“core”) as well courses that they can choose depending upon their area of interest (“elective”). The elective courses are broadly grouped into two streams comprised of several focus area as shown below:

Stream 1 Focus Areas	Stream 2 Focus Areas
Computer Science	Networking and Communication
Database and Information Systems	Embedded Systems Design
Software Engineering	Signal Processing
IT & Society	VLSI Design
	IT & Society

The broad content structure of the proposed programme is summarized in the table below.

Part	Course Type	Course Details	# Courses	# Credits	
IT	Core		12	49	
	Elective		11	44	
	Summer Internship		-	4	
	M.Tech. Thesis		-	32	
	Total # Credits of the IT Part				129
Non-IT	Core	Basic Engineering Sciences / Skills	4	17	
		Mathematics	4	16	
		Physics	2	8	
		Chemistry / Introductory Bioscience	1	4	
		Introduction to Profession	1	2	
		English	1	2	
		Technical Communication	1	2	
		Physical Education (Pass/Fail)	2	0	
	Total # Credits				51
	Elective	HSS / Management	4	16	
		Application of IT to Domains	2	8	
		Total # Credits			24
Total # Credits of the Non-IT Part				75	
Total # Credits of the Program (≈ 63% IT; ≈ 37% Non-IT)				204 (= 129 + 75)	

M.Tech. Course Structure

The M.Tech. programme is a four semester programme. The first three semesters of the programme constitute academic course work. During the fourth semester, a student can either take up an industry internship or academic research at the institute leading to a thesis.

The M.Tech. programme also includes a unique 3-week period during the beginning of the programme called “Preparatory Semester.” The preparatory semester is intended to give uniform background to all the students irrespective of their technical background. This semester covers introductory programming, mathematics and statistics and an optional course on basic electronics. After the 3-week preparatory term, the first semester of the M.Tech. programme consists entirely of required “core” courses. These teach the new student the common foundational elements required, such as advanced programming principles, mathematical background, data structures and algorithms, computer networks, digital communication, databases, and fundamentals of software engineering. The second semester builds on this foundation with two more core courses. In addition to doing the core courses, the student now has the freedom to begin focusing in one of several areas such as:

- Computer Science
- Database and Information Systems
- Embedded Systems
- IT & Society
- Networking and Communication
- Software Engineering

Across the second and third semesters, the students take at least three elective courses in their area of specialization and an additional three open elective courses (some of which may include research and other individual or group projects supervised by a faculty member). The final semester is designated for industry internship, or the writing and defense of a research thesis. The following table summarizes the overall course structure of the M.Tech.

Prep-Term 3 weeks	Prep-Term 1: Mathematics (Discrete mathematics, Probability and Statistics) Prep-Term 2: Introductory Programming (C) Prep-Term 3: Basic Electronics (Optional)	
Semester I 15 weeks	Core 1: Algorithms Core 2: Data Management	Core 3: Object Oriented Design Core 4: Networking and Communication
Semester II 17 weeks	Core 5: Operating Systems Core 6: Software Engineering	Elective 1 Elective 2
Summer Session 9 weeks	Core 7: Accounting and Finance Core 8: Marketing and Strategy Core 9: Technical Communication	
Semester III 17 weeks	Elective 3 Elective 4	Elective 5 Elective 6
Semester IV 26 weeks	Industrial Internship/Research Thesis	

Academic Infrastructure



The infrastructure for IITB's fully networked campus consists of a high-speed fiber-optic backbone connected to the internal network through a high-end gigabit Ethernet switch. Dedicated network equipment includes printers, scanners, and other equipment. Connectivity in the campus is established at two levels. First, the local intranet implements a "virtual classroom," where all the visual material, such as presentation slides used by professors in class, is made available electronically to students. All assignments and projects are announced and submitted online. The intranet also enables knowledge sharing among students.

At the second level, 24x7 Internet access is available throughout campus, in both wired and wireless modes. IITB is one of the few institutions in India to have an active wireless LAN (and was the first to have this technology way back in 1999). Wireless-mode access is available throughout campus academic and hostel blocks using the 802.11b/g protocol. In addition, high-speed Ethernet ports are installed at various locations in the building, providing wired Internet access through a proxy web server.



All students use a Wi-Fi enabled laptop for their exclusive use. This ensures a student to machine ratio of better than 1:1, implying complete accessibility. Moreover, the institute has high end computational servers. The servers provide an in-house private cloud computing infrastructure through virtualization. This in turn allows for an individual approach to learning, providing the freedom to learn at one's own pace and focus on one's chosen domain of specialization.



The Institute has an academic library with an excellent collection of books, journals, and magazines both in print and electronic form. Students have privileged free access to numerous online research resources (such as the ACM Digital Library, IEEEExplore, JSTOR).

Research Labs

Wireless Network lab -- WNL (Prof. Debabrata Das)

The lab focuses on research involving networking standards and technology. Current research work concentrates upon latest broadband wireless access technology – WiMAX/LTE. Major areas of work include, medium access control (MAC), QoS, QoE, power management, media independent handover (MIH). Microsoft Research India and TCS Research Ph.D fellowship students work in this lab.

Mobile Computing and IMS Innovation Lab (Prof. Debabrata Das)

In this lab the projects are sponsored by HP and Nokia. The R&D focus on video and audio streaming including handheld devices, as also Video Media Platform, Charging, Service Delivery in IMS using Application Servers, XDMS, HSS and Presence servers. Moreover, this lab addresses issues related to quality of service modeling in IMS architecture with respect to differential traffic.

SNIA - IIIT-B Laboratory (Prof. G. N. S. Prasanna)

The institute in collaboration with the Storage Network Industry Association host the SNIA IIIT-B Lab, an inter-industry centre for training, education network and research in all aspect of storage technology. The centre functions as a model centre in this area for academics and industry in India and South Asia in general.

Power line Communication Lab (Prof. GNS Prasanna & Prof. Jyotsna Bapat)

This lab has been sponsored by NXP. The research focuses on various aspects of power line communication for AMR over low voltage power lines. The areas include channel modeling, transceiver design and MAC layer design.

Computing Systems Lab (Prof. Shrisha Rao)

This lab focuses on systems research, with a focus on contemporary issues such as virtualization, security, reliability, energy-aware computing, high-performance computing using multicore systems, and cloud computing. There are diverse architectures and operating systems in use, such as SPARC, Mac Minis running Mac OS X 10.6, Cell Broadband Engines on Sony PS3s running Yellow Dog Linux, dual-core 64-bit AMD Opterons running FreeBSD, Open SUSE, Open Solaris, and Windows 7/XP.

Centre for Spatial Information Sciences (Prof. S Rajagopalan)

The Centre for Spatial Information Sciences (CSIS) at IIIT Bangalore was established in October 2006. The centre carries out basic and applied research in Geographic Information Sciences Domain, like Geographic Information Retrieval. GIR can be considered as a specialization of Information Retrieval, it takes into account the spatial and Object Oriented Spatial Databases; Geographic Ontology- the study of geographic objects and the relationship between them and sensor maps.

Open Systems Laboratory (Prof. Srinath Srinivasa)

The Open Systems Laboratory (OSL) at IIIT Bangalore was started in 2002. It works in the broad areas of data and information systems engineering, graph data management, web information retrieval, text mining, social network

analysis, mobile data management, distributed computing and openworld computing. The lab also hosts the first PlanetLab (www.planet-lab.org) node in India. PlanetLab grid is a worldwide grid for testing distributed algorithms. The OSL is also involved in another major project called Silverfish, whose objective is to develop a wide-area data grid for academic materials and course pages.

Software Design Laboratory (Prof. K. V. Dinesha)

The research focus here is on the design and architecture of software. Design patterns approach for the software development process is studied, with special emphasis on the impacts of design principals and patterns on the flexibility as one of the prime focus.

Document Engineering Lab (Prof. Chandrashekar Ramanathan)

Documents still constitute a significant content type in the enterprise today. Document Engineering deals with developing algorithms, techniques, tools and processes that help in creating and manipulating the content, format, and representation of documents. There are several challenges being addressed as part of Document Engineering. The DocEng lab explores the various standards and tools available in this space. Following are some of the projects from the Lab:

- Pralekhasaara (for interactive content chunking and assembling)
- ORCA (Online Repository for Content Assembly)
- DocuBhasha (translation of documents, supported by Microsoft Research)

Information Convergence Lab (Prof. Chandrashekar Ramanathan)

The revolution of mobile phones made media convergence possible. Today we don't need separate devices for talking on the phone, listening to music, watching videos, surfing the Internet, reading/sending e-mails. Information convergence is a similar concept that is focused on interoperability of information scattered across multiple dimensions and multiple sources and destinations. The focus of the Information

Convergence Lab (I-COG Lab) is to first identify and define various information convergence challenges that are relevant to the real world. The current focus of the lab is to start with a study of information convergence challenges specifically targeted at large enterprises and the government. Based on this understanding of the needs and contexts of information convergence, the lab will specify reusable frameworks that address these challenges in a unified and integrated environment. The focus would be to develop standards-based solutions that can be applied widely.

Center for Electronics and Embedded Systems (Prof. P G Poonacha)

The Center for Electronics and Embedded Systems (CEEMS) Lab's objective is to nurture talent by focusing on Embedded Computing, Wireless Communication and Computer vision. Facilities available in the lab will enable world class research and education and will be seen as an extension of the strong Information Technology (IT) Core competence already available at IITB. CEEMS Lab collaborates with public and private organizations to do research and development in the emerging areas of embedded systems to bridge the gap between academia output and industry requirements thus providing every learner an equal opportunity to become industry ready.

CEEMS lab is funded by the government of Karnataka.

Wireless Sensor Network (WSN) Lab (Prof. Jyotsna Bapat)

Machine to machine (M2M) communication is going to be one of the major areas of R&D in networking and communication specialization. M2M faces multiple challenges and some of the major issues related to efficient communication between sensors, protocols, power saving in sensor, etc. In IITB we have a WSN lab which supports multiple R&D projects on sensor networks. This lab has been sponsored by Govt. of Karnataka for development in embedded systems.



Tuition, Aid & Scholarships

The tuition for the various programmes at IITB is shown below:

Integrated M.Tech.	Rs. 75,000/- per semester
M.Tech.	Rs. 60,000/- per semester plus Rs 30000/- for the summer semester
MS by Research	Rs. 60,000/- per semester (Payable only for the first 3 semesters)
Ph.D	Rs. 60,000/- per semester (Payable only for the first 3 semesters)

The fees are revised every two years. The fees payable by the integrated M Tech students in the years 3, 4 and in year 5 will be as per the fees in vogue at that time. M.Tech, MS and Ph.D students pay the fees that are in vogue at the time of their admission. In addition, residential students pay Rs. 3000 towards hostel rent per month and approximately Rs. 3000 per month for food expense. Other costs for books and supplies, travel, purchase of a laptop, etc., need to be factored by students depending on their individual circumstances.

Most M.Tech. and Integrated M.Tech. students receive bank loans at fairly generous terms that cover all their costs. About 25% of the students are eligible for merit-based, industry-sponsored scholarships. Teaching assistantship opportunities, which provide a financial incentive besides valuable experience for future careers, are available to senior M.Tech. students based on their academic performances in the first year.

Employed students who undertake studies at IITB are expected to cover their own costs, including tuition. Many such students are sponsored by their employers.

All full-time Ph.D. students, except those receiving support from other sources, are offered full financial support covering tuition and a stipend of Rs. 25000 per month. Such funding for our Ph.D. students comes from HP, IBM, Intel, Infosys, Motorola, Siemens, and other companies.

Industry-Funded Scholarships

The Institute has offered about 15 scholarships (out of about 150 students admitted in 2011) to meritorious students joining the M.Tech programme. The institute expects to offer a similar number of industry-funded scholarships in 2012 as well. The scholarship amount covers almost all student expenses over the duration of their M. Tech. The selection for these scholarships is carried out by the industry representatives along with IITB faculty. 2010-11 scholarship sponsors include; Infosys, ABB, Siemens, Huawei and SocGen. Similar scholarship schemes are planned to be extended for Integrated M.Tech. students as well in the future.

Internship

For M. Tech. students, the Institute's internship programme gives the students an opportunity to get hands-on exposure in real-world projects as part of the final semester. Many of the companies that participate in the final Placement Programme also participate in the Internship Programme. This allows students to spend one whole semester in the industry working on live projects prior to graduation. The companies provide students with stipends for the duration of the internship and also the opportunity to work with their teams, at their premises, on challenging real-life projects. The internship programme is an avenue for placement, as many interns who perform well are given job offers by the companies they work for in the final semester.

In addition to the industry-sponsored internships based out of India, there is also an opportunity to do research and project work at the Technical University of Kaiserslautern and Hof University in Germany as part of the final semester internship. Some of our students have also gone to National University of Singapore as Research Assistants during their internship tenure. The internship includes financial aid to cover travel and living expenses in Germany / Singapore during the course of internship.

Integrated M. Tech. students will be pursuing a summer internship at the end of third year. This will provide them with an exposure to work on industrial projects.

Placements

The Institute has a strong placement programme that has achieved 100% placement of all its graduates. The graduates have found challenging assignments with several multinational and Indian giants of the IT industry. A broad-based curriculum coupled with strong bonds with the industry ensures that the students are equipped with the right skills to be highly productive and ready to take on real-world IT challenges when they graduate.

IIIT-B has enjoyed excellent placement for all the eleven batches of M.Tech students who have graduated so far. Of the 1396 alumni, a few have gone for doctoral studies at MIT, UC Berkeley, Indiana University, University of Washington and Syracuse University in USA, Paderborn and University of Kaiserslautern in Germany, Trento in Italy and IIIT-B and BITS Pilani in India. The rest have taken up positions in more than 180 corporations that include:

- **IT Products Majors** – AmDocs, Ariba, Adobe, BEA, Business Objects, Compaq, Digital, EMC, HP, IBM, Intel, iFlex, i2, Microsoft, Novell, Oracle, SAP, SAS, Software AG, Sun, Symantec, Unisys, Verisign.
- **R & D units** - ABB Research, C-Step, Daimler Chrysler Research, Google Labs, HP Labs, Honeywell, IBM Research, Infosys SETLabs, Microsoft Research, Siemens Corporate Research, Yahoo Labs, Nokia Research
- **IT Services Majors** - Acenture, Cap Gemini, Caritor, Cognizant, Convansys, CSC, EDS, HCL, Infosys, IBM, Mindtree, Mastek, Satyam, Symphony, TCS, Wipro, Zensar.
- **Engineering Majors** - ABB, Ashok Leyland, Bosch, Bosel, Daimler Chrysler, Delmia, Delphi, Dassault, GE, General Motors, Honeywell, Mercedes Benz, Philips Systems, Siemens.
- **Networking Majors** - Alcatel, Cisco, D-Link, Fiber Link, Huawei, Lucent, LG-Soft, Motorola, Nokia, Nokia Siemens, Nortel, Qwest, Samsung
- **Semiconductor Majors** - Agere, Freescale, Intel, Infineon, Texas Instruments, NXP, Infineon, Samsung.
- **EDA Major** - Cadence, Magna, Magma, Mentor Graphics
- **Mid size Companies** - Birla Soft, Bailey, Celstream, Fiber Link, GAVS, GT Nexus, IP Horizons, Patni, Ramco, Sasken, Symphony Services, Thorogood, Thought Works, Velankani, Visual Soft, Web Methods
- **Internet companies** - Amazon, AOL, Microsoft, Yahoo, TV18, Web18
- **Start up Companies** - 2' Create, 8k Mile, Aalayane, Alacito, Allgo, Alopa, Altron, Backend, Bangalore Labs, Bluefont, Cerner Health Care, Capillary, C-Core, Cell, Cerner, Next, Customer Asset, Dev Square, e2e Solutions, egestalt, Fast Media, GE GXS, Globals, Glomantre, Indus Technovation, Intellinate, Ittiam, Kenfuse, Kolabia, Lattice Bridge, M Formation, Mapunity, Mango Tech, Manmar, MarketIntelligent, Marvell India, Perfont, Proteam, Pramati, Pro Medik, QSO, Radix, Real Metrix, Readimindes, Red Bus, Riverstone, S7, Sonic Wall, Suruk, Techvoyant, TLL, Tutor Vista, Tyfone, Vision Labs, Xore, Yodlee, Yos, Ziva Software, Zive
- **FMCG Majors** - CocaCola, P & G
- **Banking Insurance Majors** - American Express, BOA, Deutsche Bank, Fidelity, HSBC, Citigroup
- **Public Sector** - BEL, SAIL, HPCL
- **Others** - TESCO, SABRE, Goldman Sachs, CDC Software, Informatica

Academic Outreach Activities

IITB exposes its students to the industrial outlook through a multitude of conferences, workshops and other events. Some major international conferences were hosted by IITB in 2011:

- 12th International Conference on Distributed Computing and Networking (**ICDCN 2011**) was held at IITB campus during January 2-4, 2011. The conference was organized by Infosys.
- 20th International World Wide Conference (**WWW 2011**) was held between March 28 and April 1, 2011. IITB was the host institution for the conference and the conference was held in Hyderabad, India.
- 3rd International Conference on Human Computer Interaction (**India HCI 2011**) was held at IITB during April 7-11, 2011.
- 5th International Conference on Internet Multimedia Systems Architecture and Application (**IMSAA 11**) was held in IITB during December 12-13, 2011. This is the 5th year the conference is being organized and hosted by IITB.
- 17th International Conference on Management of Data (**COMAD 2011**) was held in IITB during December 19-21, 2011.

Social & Cultural Activity

IITB organizes inter collegiate technical fest titled “Innofest”. Large number of students from prestigious institutions participate in the fest. Innofest includes events like online programming contest, LAN Games, web development, and paper presentations.

IITB understands its social responsibility and has its students working for it under the name AIKYAM. Our mission includes giving back to the underprivileged children of our society. Some of our activities include teaching spoken English, spreading computer awareness and organizing theater workshop for these children.

Along with Infosys, IITB conducted a Special Training Programme (STP) that helped nearly 100 students belonging to the underprivileged sections of the society. The experiment undertaken in 2007-08 got repeated at IITB in 2008-09.

Admissions

IITB will admit about 60 students for the Integrated M.Tech. programme. Prospective candidates for the Integrated M.Tech. programme should have done their 10+2 or equivalent level with Mathematics and must have obtained first class or equivalent. Students expecting to complete 10+2 or equivalent by August 2012 may also apply.

Typically IITB admits about 125-150 students every year into the flagship M.Tech. Programme in IT. The minimum qualification for admission to M.Tech. programme in the Institute is a first-class four-year bachelor's degree in engineering (i.e., B.E., B.Tech., or equivalent). M.Sc./MCA degree holders can also apply. Final-year students expecting to graduate by August 2012 may also apply.

For the Ph.D. programme, applications from candidates holding Master's degrees in a technical discipline (i.e., an M.E., M.Tech., or equivalent) will be preferred, but candidates who possess just the minimum qualification but show great aptitude for research (e.g., as shown by research publications in international conferences or journals, or patent applications) may be considered. For the Ph.D programme in Information Technology and Society those holding Master's Degree in Social Sciences including Economics will also be eligible to apply.

Candidates who are perceived to lack sufficient research skills but show promise may sometimes also be advised to pursue the M.S. by Research first, prior to being accepted into the Ph.D. programme.

Admissions are generally made to Indian citizens living in India. We are open to receiving applications from outstanding foreign applicants.

Integrated M.Tech. Admissions

Eligibility

Minimum Requirements

The minimum qualification for admission to the Integrated M.Tech. programme at IITB is a first-class in 10 + 2 (or equivalent) with Mathematics as one of the subjects. Students expecting to complete the minimum requirement by August 2012 may also apply. The detailed list of qualifying examinations is given below.

DETAILED LIST OF QUALIFYING EXAMINATIONS

1. The +2 level examination in the 10+2 pattern of examination of any recognized Central/State Board of Secondary Examination, such as Central Board of Secondary Education, New Delhi, and Council for Indian School Certificate Examination, New Delhi
2. Intermediate or two-year Pre-University Examination conducted by a recognized Board/University.
3. Final Examination of the two-year course of the Joint Services Wing of the National Defence Academy.
4. Any Public School/Board/University Examination in India or in foreign countries recognized by the Association of Indian Universities or any state or central government as equivalent to 10+2 system.
5. H.S.C. Vocational Examination.
6. Senior Secondary School Examination conducted by the National Institute Open Schooling (NIOS) with a minimum of five subjects.
7. 3 or 4-year diploma recognized by AICTE or a State Board of Technical Education.

The applicant must have taken Mathematics as one of the subjects in the above qualifying examination.

All the candidates must secure at least 60% marks in aggregate in qualifying examination. The percentage of marks awarded by the Board will be treated as final. If the Board does not award the percentage of marks, it will be calculated based on the marks obtained in all subjects listed in the mark sheet. If any Board awards only letter grades without providing an equivalent percentage of marks on the grade sheet, the candidate should obtain a certificate from the Board specifying the equivalent marks, and submit it at the time of counseling / admission. In case such a certificate is not provided the decision taken by the IITB Admissions Committee shall be final.

Selection Procedure

Admissions to the Integrated M.Tech. programme is solely on the basis of performance at the All-India Engineering Entrance Examination (AIEEE 2012) conducted by CBSE in the year 2012.

Important Dates

Online Applications	
Last date for receiving online applications	June 22, 2012
Last date for receiving application fee	June 30, 2012
Selection and Joining	
Notification of selected candidate on the candidate portal <i>(to be checked by the candidates by signing in to the portal)</i>	July 07, 2012
Open house (campus tour, etc.)	July 11, 2012
Remittance of acceptance fee	July 20, 2012
Joining date for the new batch	July 30, 2012
Induction and course registration	July 30 - Aug 3, 2012
Classes begin	August 6, 2012

M.Tech. Admissions

Eligibility

Minimum Requirements

The minimum qualification for admission to the M.Tech. programme at IITB is a first-class four-year bachelor's degree in engineering (i.e. B.E., B.Tech., or equivalent). First class degree holders in 3-year undergraduate programmes followed by Masters Degree with first class in any of the physical sciences, or an MCA degree with first class, can also apply. Final-year students expecting to graduate by August 2012 may also apply. First class or equivalent is required in 10th and 12th (or equivalent) grades as well.

Selection Procedure

Admissions to the M.Tech. programme is on the basis of prior academic performance (at the high school level and beyond), performance in the IITB's online admissions test and an admission interview. Applicants with good scores in the GATE examination and GRE examination may be eligible for waiver of Entrance Examination (see below).

Entrance Exam Waiver

Candidates with GATE Scores

Applicants having valid GATE scores from selected disciplines are welcome to apply for the M.Tech. programme. Such applicants may qualify for waiver of the entrance examination. GATE scores from the following disciplines will be considered for this purpose:

- Aerospace Engineering ➤ ECE ➤ Mechanical Engineering ➤ Production and Industrial Engg
- Civil Engineering ➤ Electrical Engineering ➤ Mining Engineering
- Computer Science and IT ➤ Instrumentation Engineering ➤ Metallurgical Engineering
- Chemistry ➤ Mathematics ➤ Physics

Candidates with GRE Scores

Applicants who meet the minimum requirements and have valid GRE General Test scores are welcome to apply for the M.Tech. programme. Such applicants may also qualify for waiver of the entrance examination.

International Students

Foreign nationals (FNs) and non-resident Indians (NRIs) are welcome to apply for the M.Tech. programme. Such applicants can obtain a waiver of the entrance examination with valid GRE and TOEFL scores. Only those students whose undergraduate education was in an Anglophone country (i.e. Australia, Canada, New Zealand, Singapore, UK, and the US) are exempt from TOEFL. FN and NRIs who are short listed for the interview, can also seek waiver of the interview by submitting three letters of recommendation.

Working Professionals

Working professionals applying for the M.Tech. programme must get leave for the duration of the M.Tech. programme. Such applications have to be sent through the employer (or accompanied by a letter from a supervisor or other authorized representative of the employer stating that there is no objection to the applicant's joining IITB as a student). Working students will also need sponsorship from the employer, or obtain support from other sources to meet their costs, as scholarships and other financial support are not available to working students.

Important Dates

Online Applications	
Last date for applying online	March 18, 2012
Last date for receiving Application Fee	March 23, 2012
Exams & Interviews	
Notification of exam waiver for qualified applicants	March 24, 2012
Exam Dates: Students are provided with two choices for taking the online admission test	Date 1: April 1, 2012 Date 2: April 8, 2012
Selection interviews at IITB campus	May 2-4, 2012
Selection and Joining	
Notification of selection	May 15, 2012
Remittance of acceptance fee	June 1, 2012

Remittance of balance fee (1 st sem, hostel, etc.)	July 13, 2012
Joining date for new batch	July 13, 2012
Course registrations through portal	July 13-14, 2012
Classes begin	July 16, 2012

MS by Research Admissions

Eligibility

Minimum Requirements

The minimum qualification for admission is a first-class four-year bachelor's degree in engineering (i.e. B.E., B.Tech., or equivalent). M.Sc./MCA degree holders can also apply. Final-year students expecting to graduate by August 2012 may also apply. We are open to receiving applications to the research programmes (particularly for the Ph.D.) from outstanding international students.

Working Professionals

Working professionals may apply for the M.S. by Research programme. Such applications have to be sent through the employer (or accompanied by a letter from a supervisor or other authorized representative of the employer stating that there is no objection to the applicant's joining IIITB as a student). Working students will also need sponsorship from the employer, or obtain support from other sources to meet their costs, as scholarships and other financial support are not available to working students.

Selection Procedure

Admission to the M.S. by Research programme is on the basis of submitted records of prior academic work and other scholarship showing aptitude for research, letters of recommendation, and a personal interview.

Key Dates

Online Applications	
Last date for receiving online applications	May 23, 2012
Last date for receiving application fee	June 1, 2012
Selection and Joining	
Short listing of candidate for interviews	June 10, 2012
Interviews at IIITB	End of June, 2012
Joining and course registration	August 3, 2012
Classes begin	August 6, 2012

Ph.D. Admissions

IITB invites applications for the Ph.D. programme for the academic year starting in August 2012. Visit our website for details.

Eligibility

Minimum Requirements

The usual minimum qualification required is a 1st class four-year degree in a technical discipline (i.e., a 1st class B.E., B.Tech, or equivalent), but people holding 1st class Master's degrees in the mathematical sciences (e.g. an M.Sc. in mathematics, physics, statistics, or electronics) may also apply. Ordinarily, applications from candidates holding Master's degrees in a technical discipline (i.e. an M.E., M.Tech, or equivalent) will be preferred. Working professionals are required to either take leave for the entire duration of study or obtain a relieving order from the current employer at the time of joining the full-time Ph.D. program.

Admission to the Ph.D. programme is on the basis of submitted records of prior academic work and other scholarship showing aptitude for research, letters of recommendation, and a personal interview.

Key Dates

Online Applications	
Last date for receiving online applications	May 23, 2012
Last date for receiving application fee	June 1, 2012
Selection and Joining	
Short listing of candidate for interviews	June 10, 2012
Interviews at IITB	End of June, 2012
Joining and course registration	August 3, 2012
Classes begin	August 6, 2012

How to apply

Online Application Form

Candidates can apply for all the programmes using online application only.

Online Application Portal

Access the IITB Online Application site (<http://iiitb.in>) and fill in the online application form.

- Step a) Click on **Register as New Applicant** and create your User ID and Password for IIT-B Online Admissions Portal. You will receive an email notification with your User ID and Password after successful registration. This User ID and Password can be used to login to the Online Admissions Portal to Apply for relevant programme at IITB, to check the status of your Application Fee payment and to Download the Admit Card once it is generated.
- Step b) Login to the portal using your UserId and Password.
- Step c) Read the **Instructions** to fill the application form. Also read the **Terms and Conditions**.
- Step d) Click on the **“Apply”** link corresponding to the programme you are applying (M.Tech, Integrated M.Tech., MS by Research, or PhD) from the **“Application Form”** page to go to the corresponding Online Application Form.
- Step e) There are 3 pages for the application form – The **“Personal Information”** page, the **“Enrollment Information”** page and the **“Competitive Exam Details”** page. You may “Save” the partially filled application form, logout and come back to it later to complete it. All the mandatory fields need to be filled before the final submission of the application form. A unique Candidate ID gets assigned to the application on the first Save of the Application Form. Once the Application Form is submitted - it cannot be edited.
- Step f) Applicants need to fill the Payment Details after submitting the application. Link is provided on the application form for the Payment page. You can also go to **Application Form** home page and click on **View/Edit** link under **Payment Info**.

On the **Payment Information** page - Click on **Pay Now** button for Application Fee to fill in the Bank Challan or Demand Draft details. Click **Submit** to submit the payment details. The payment status is changed to **“Sent”** when the information is submitted. Once the IITB office admin receives the Challan/DD in mail they will update the status of your payment to **“Paid”**. Then only your application will get considered for admission.

Applicants can change their Password or Email Address in the system from the **“User Information”** page.

Payment Information

Step a) All candidates need to pay a non-refundable application fee of **Rs.1000/-**. There are two options for making the payment as explained below:

1. DEMAND DRAFT:

Get a demand draft from any Bank in favor of "International Institute of Information Technology Bangalore" payable at Bangalore. Write your name and application ID at the back of the DD.

Choose the mode of Payment as DD in "Make Payment" Tab and click "Submit". Take a print of the Payment advice form and sign it.

Send the following to the IIITB address given below:

- Original demand draft
- Print out of your Online Application Form (You can go to **Preview**, click on **view Application** to take a print out of it.)

International Institute of Information Technology, Bangalore

26/C, Electronics City,
Hosur Road, Bangalore - 560100

2. Cash at SBI Bank (Payment through challan)

You may choose to pay the application fee through cash. Select the option of Payment through **SBI Bank branch** and take the print out of the SBI Pay-in-slip. This pay-in-slip consists of 3 parts – one for bank's record, one for your own record and the third for sending to IIITB for completing the registration process. You need to pay the cash at any SBI Bank branches in your city. On making the payment, the bank will return two parts of the pay-in-slip to you, one of which will be the receipt for you.

Send the following to the Collection Center address given below:

- Signed Pay-in-Slip (IIITB Copy)
- Print out of your Online Application Form (You can go to **Preview**, click on **view Application** to take a print out of it.)

International Institute of Information Technology, Bangalore

26/C, Electronics City,
Hosur Road, Bangalore - 560100

Step b) IIITB will authenticate the payment details and will update the portal status confirming the payment for IIITB application form. Candidates can login to online application and visit the Application Summary to know the status of payment.

M.Tech. Online Entrance Examination Details

Applying for Entrance Exam Waiver

All the candidates who are applying for M.Tech. Programme and enter valid GATE / GRE details will be automatically considered for waiver of IITB Entrance Examination. Notification of waiver of Entrance Exam will happen by **24th March 2012**.

e-HALL TICKET

e-hall ticket is the admit card that is sent to all the M.Tech. candidates whose online application forms have been processed successfully. The link to download the e-hall ticket will be made available at the portal. Candidates must login to the portal and take two print outs of the e-hall ticket. Please take the print outs on A4 size paper only. Please ensure that all information on the e-hall ticket is clearly visible on the print out.

NO CANDIDATE WILL BE PERMITTED TO APPEAR FOR THE TEST WITHOUT A VALID HALL TICKET.

The e-hall ticket is an important document that must be preserved and produced at the time of the online admission test and also at the time of interview/admission as needed.

Test Centers

The online admission test will be held at 12 centres across India as per the following schedule:

April 1st 2012	Bangalore	Hyderabad	
	Chennai	Mumbai	
	Delhi		
April 8th 2012	Ahmedabad	Chennai	Kolkata
	Bangalore	Cochin	Lucknow
	Bhopal	Delhi	Mumbai
	Chandigarh	Hyderabad	Pune

The institute will do its best to accommodate the requests for center allocation as per the given preferences but candidates may be allocated centers other than those indicated in the preferences in the application form.

Online M.Tech. Admission Test Guidelines

The salient features of the computer-based ONLINE admission test are:

- The candidate sits in front of the computer and the questions are presented on the monitor. The candidate submits his/her answers through the use of mouse. The computer is connected to the server, which delivers the test, in real time through a reliable connectivity.
- The online admission test assumes that the candidate has basic familiarity with the use of computers like use of keyboard and mouse operation. It is the responsibility of the candidate to acquire these skills before appearing in the test and the Institute cannot take responsibility for the same.
- In the rare and unlikely event of a technical failure during the test, the candidate may be required to

attempt the test again.

- Candidate must visit the Institute's website <http://www.iitb.ac.in> regularly to obtain latest news, information and updates on the online admission test.

Ph.D. Applications

Apply online as per the procedure given above and submit the following with a covering letter giving a summary, to:

International Institute of Information Technology, Bangalore

26/C, Electronics City,
Hosur Road, Bangalore - 560100

- *A printout of the duly filled online application form*
- *Transcripts or attested copies of prior academic work, and copies of any published papers.*
- *A statement of purpose of between 1000-2000 words, indicating research goals and prior research experience.*
- *A non-refundable application fee of Rs. 1000, in the form of a demand draft payable to IIIT-Bangalore.*

In addition, arrange to have three letters of recommendation supporting the application sent directly to the Registrar (Such letters should be from former Professors, research supervisors, or other qualified people familiar with the applicant and able to describe and attest to his/her scholastic ability and aptitude for first-rate research.).

M.S. by Research Applications

Follow the procedure outlined above for the Ph.D. applications, indicating that your interest is in the M.S. by Research rather than the Ph.D.

Address for Correspondence

All correspondence including the application form, proof of payment and other related documents must be sent to the following address:

Registrar
International Institute of Information Technology, Bangalore

26/C, Electronics City,
Hosur Road, Bangalore - 560100

Tel. (080) 4140 7777 / 2852 7627 Fax: (080) 28527636

e-mail: admissions@iitb.ac.in

FACULTY PROFILE

Jyotsna Bapat (Ph.D., Penn State University, USA)
jbapat@iiitb.ac.in



Prof. Bapat received her Ph.D. from Penn State University. Her thesis was in the area of semi-blind equalization applied to communication systems. After graduation, she worked on design and implementation of voice-band (V.34) and DSL (G.lite) modems at Ariel Corp and Lucent Technologies respectively. Her area of interest is Digital Signal Processing as applied to communication systems. In particular, she is interested in semi-blind identification as applied to OFDM systems.

Debabrata Das (Ph.D., Indian Institute of Technology, Kharagpur)
ddas@iiitb.ac.in



Prof. Das received his Ph.D. degree from the Indian Institute of Technology, Kharagpur (IIT Kharagpur). At present he is serving as Hewlett Packard Chair and Associate Professor at IIIT-Bangalore. Before joining IIIT-B, he had served at G S Sanyal School of Telecommunication at IIT Kharagpur and later at Kirana Networks in New Jersey, USA. At present he is PI of Department of Information Technology, Government of India Sponsored project on Green Broadband Wireless Network and a Nokia sponsored project in the area of Mobile Computing and Hewlett Packard supported on IP Multimedia Services (IMS). His areas of teaching interest are, Wireless Access Network, Mobile Computing with IMS and Internet-working. His main areas of research interest are Wireless Access Network's MAC, QoS, Power saving and IP Multi-media Subsystems.

Prof. Das has more than 45 peer reviewed papers in different journals and international conferences.

K. V. Dinesha (Ph.D., Indian Institute of Technology, Bombay)
dinesha@iiitb.ac.in



For the past 20 years, Prof. Dinesha has been involved in teaching, research and consultancy in Information Technology. He has been developing scientific and commercial software. His areas of interest include Software Engineering, Quality Systems (ISO and SEI CMM Models), Cryptography, Object Technology and Data Structures.

Jayprakash Lalchandani (Ph.D., Indian Institute of Technology, Kharagpur)
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JayPrakash joined the institute in January 2012. He completed his PhD in Computer Science (specialization: Software Engineering) from the Indian Institute of Technology Kharagpur (IITKGP) in 2010. Before joining IIIT-B, he was associated as a post-doctoral researcher with the General Motors Collaborative Research Laboratory in the Department of Computer Science and Engineering at IITKGP. His current research interests include program and model analysis, testing, requirements engineering, etc.

Meenakshi D' Souza (Ph.D., Institute of Mathematical Sciences, India)

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Prof. D' Souza did her Master's in Mathematics from University of Madras, Chennai and her Ph. D. in Theoretical Computer Science from The Institute of Mathematical Sciences, Chennai. She joined the research department of Honeywell Technology Solutions, Bangalore soon after completing her Ph. D. and worked there in the areas of Formal Verification of Software Design, Model Based Development and Physical Access Control. Her research interests are in Formal Methods, Model Based Development, Automata Theory and Enterprise Security. She is also interested in Research and Technology Strategy and Intellectual Property Rights Management.

Sumit Mediratta (Ph.D., University of Southern California)

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Professor Sumit Mediratta received BE in Electronics and Telecommunication Engineering from Shri Govindram Seksaria Institute of Technology and Science (SGSITS), Indore. After that, he joined University of Southern California, Los Angeles to pursue his masters and doctoral studies. The focus of his MS was on VLSI Design and his PhD was on interconnection networks. After completing PhD, he served NVIDIA Graphics Pvt. Ltd. at the positions of Senior Platform Architect and Senior ASIC Design Engineer. At NVIDIA, he was involved in variety of design and verification activities related to various aspects of Media and Communications Processors and discrete Graphics Processing Units (GPUs). He also served General Motors at the position of Senior Researcher. There, he initiated few research projects in the domain of automobile E/E (electrical and electronics) architecture. He has published several papers in international conferences and journals. He also authored several internal technical publications during his industry experience.

His current areas of interest are: - Embedded systems; Digital signal processing; Reconfigurable computing architectures; Interconnection networks; Architectures of advanced SOCs, GPUs and CPUs; Automotive electrical and electronic system architectures; Fault-tolerant computer systems; Performance modeling, analysis and evaluation.

V. N. Muralidhara (Ph.D., Indian Institute of Technology, Delhi)

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Prof. Muralidhara has done PhD in Computer Science and Engineering at Indian Institute of Technology Delhi. His thesis was in the area of Algorithms. He has done M.Tech. in Computer Application at IIT Delhi and M.Sc. in Mathematics at University of Hyderabad. Before Joining IIIT Bangalore, he has worked as Research Associate at SERC, Indian Institute of Science Bangalore for few months. He is interested in the theory of algorithms and complexity, and its applications. More specifically, his broad areas of research interest include combinatorial optimization, approximation algorithms, randomized algorithms, on-line algorithms, cryptography, algebra and coding theory.

Srinath R. Naidu (Ph.D., Eindhoven University of Technology, Netherlands)

srinathn@iiitb.ac.in



Prof. Naidu obtained his B. Tech. degree from the Institute of Technology, BHU in 1996. After completing his Master's degree from the Indian Institute of Science, Bangalore in 1998 he went on to finish his Ph.D. in the area of statistical timing analysis for digital integrated circuits from Eindhoven University of Technology in 2004. After completing his Ph.D., he worked for Magma Design Automation Inc in the area of statistical timing analysis. His last work assignment before joining IIITB was with Cadence Design Systems in the area of low power synthesis. His research interests are mainly in the area of electronic design automation including statistical timing analysis and optimization for digital circuits, power analysis and optimization and formal verification. He is also interested in combinatorial optimization, and design and analysis of algorithms.

Jaya Sreevalsan Nair (Ph.D., University of California, Davis, USA)

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Prof. Nair obtained her Ph.D. in Computer Science from University of California, Davis; after a B. Tech in Aerospace Engineering from IITM and a M.S. in Computational Engineering from Mississippi State University. Prior to joining IIIT-B, she worked as a scientific programmer at Enthought Inc. Austin and as a research associate at Texas Advanced Computing Center, University of Texas at Austin. Her areas of interest are scientific computing, scientific visualization, computer graphics, and computational geometry.

Poonacha P G (Ph.D., Indian Institute of Technology. Kanpur)

poonacha@iiitb.ac.in



Poonacha received B.Tech degree in Electronics and Communication Engineering from KREC (now known as NITK) Surathkal in 1978, M.Tech and Ph. D. degrees in Electrical Engineering from IIT Kanpur in 1981 and 1987 respectively. He is a Senior member of IEEE. He has been working in Signal and Image processing and digital communication areas for the past 24+ years which includes 10 years at IITB and 14 years in industry.

He was a faculty member in the EE Department at IIT Bombay between 1986 and 1996. During that period he taught PG level courses in the area of Digital communications, Adaptive signal processing and Neural Networks and worked as Principal as well as co-investigator on MHRD projects by government of India. He was with Sasken from 1996 to 2002. At Sasken he became Vice President of its R&D division in 2001, responsible for R&D initiatives and patents in the area of 4G wireless, MPEG7 and Software Defined Radio (SDR). He was also a member of the management council at Sasken in 2001. Prior to that, he was the head of 3G wireless program at Sasken responsible for development of 3G protocol stack and baseband. Before joining Epigon he worked as a Senior Technologist at Texas Instruments, Bangalore. He worked as Chief Technology Advisor and Director at Epigon Media Technologies from 2003 to 2011. At Epigon he worked on development of H.264 codec, Worldspace hybrid radio receiver, DAB receiver and OFDM based modem development.

Research Interests: Computer Vision Problems, Wireless Communications and Wireless Sensor Networks, Software Defined Radio, Neural Networks and their applications.

Balaji Parthasarathy (Ph.D., University of California, Berkeley, USA)

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Prof. Parthasarathy obtained his Ph.D. from the University of California at Berkeley. His research broadly focuses on the relationship between technological innovation, economic globalization, and social change. Within this broad focus, his work follows two threads. One thread examines the impacts of public policy and firm strategies on the organization of production in the ICT (Information and Communications technology) industry. Another thread deals with "ICTs for Development," or ICTD. Here his interests lie in understanding how ICTs are being deployed in various domains of activity to transform social relationships, especially in economically underdeveloped contexts.

G. N. S. Prasanna (Ph.D., MIT, USA)

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Prof. Prasanna did his B. Tech at IIT Kanpur, and MS and Ph.D. at MIT, USA. He has worked at Lucent Microelectronics and Lucent Bell Laboratories, for about 11 years. At Lucent he worked in a variety of fields, including VLSI, switching, optical networking, etc. He was responsible for the signal processing system design of a major access product for Lucent's 5ESS switch, accounting for 30 million lines world-wide. Recently his research interests have included optical networking and robust optimization. He has published about 35 papers, and holds about 15 patents. He has been on several technical program committees and has

served as a referee for several journals. He is interested in communication systems (optical, wireless, power line), robust optimization, electromechanical systems, animation and mathematics.

Niladri Bihari Puhan (*Ph.D., Nanyang Technological University, Singapore*)
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Prof. Puhan received his PhD from Nanyang Technological University (NTU) Singapore (2007) and M.E in Signal Processing from Indian Institute of Science (IISc) Bangalore in 2002. He was involved in postdoctoral research from 2007 to 2009 in Nanyang Technological University. He joined as an Assistant Professor at International Institute of Information Technology (IIIT) Bangalore in January 2010. His current research is focused on designing novel, clever and practical methods in Digital watermarking, Biometrics, Image Processing and Image Understanding.

S. Rajagopalan (*Ph.D., Indian Institute of Technology, Kanpur*)
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Prof. S. Rajagopalan received his B. Tech degree from IIT Delhi, PGDM from IIM Bangalore and Ph.D. from IIT Kanpur. He was the Chief Executive Officer of the Karnataka State Council for Science and Technology from 1982 to 1993 and was involved managing innovations that addressed the problems of Karnataka. In 1993 he along with a few colleagues founded Technology Informatics Design Endeavour (TIDE) a non-profit development society which focused on developing and disseminating technologies that are economically attractive, environmentally sustainable and socially acceptable in rural areas of Karnataka. He was its Chairman till 2007. For his work in TIDE, he was awarded the Ashoka Fellow (1994), Fellow of the Salzburg Seminar (1999), one of the four finalists of Social Entrepreneur of India award (2006) and one of the 50 pioneers of India chosen by India Today Magazine (2008). TIDE was awarded the International Green Oscar, the Ashden Award in 2008.

Prof. Rajagopalan has been working in the area of Geographical Information Systems since 1989 and founded a Company Spatial Data Private Limited in 1999. This company was pioneer in developing user-friendly digital maps of Indian Cities under the Brand Name Mapcue. Dr. Rajagopalan's areas of interest include innovation dissemination, economics of innovations, Geographical Information systems, and Economics of Information Technologies. He also heads the IIITB Innovation Centre, which aims at incubating and promoting innovations in the area of Information and Communication Technologies.

Chandrashekar Ramanathan (*Ph.D., Mississippi State University, USA*)
rc@iiitb.ac.in



Prof. Ramanathan received his Ph.D. degree from Mississippi State University. His thesis was in the area of object-oriented databases. He has extensive application software development experience in large multinational organizations. Most recently, he was working as a senior architect at Hewlett Packard. His current focus is in the area of databases and software engineering. Application architectures, enterprise content management and knowledge management are his other areas of interest.

Shrisha Rao (*Ph.D., University of Iowa, USA*)
srao@iiitb.ac.in



Prof. Rao obtained his Ph.D. in computer science from the University of Iowa, and also has an M.S. in logic and computation from Carnegie Mellon University. His primary re-search interest is in the area of distributed computing, specifically algorithms and formal methods for concurrent and distributed systems. He also has interests in problems such as distributed fair division and demand-side management. He occasionally dabbles in Mathematics. He is also a regular reviewer for the ACM Computing Reviews journal.

S Sadagopan (Ph.D., Purdue University, USA)*s.sadagopan@iiitb.ac.in*

Professor Sadagopan, Director of IIIT-Bangalore, is a product of Madras University, India and Purdue University, USA. He taught for 25+ years at IIT Kanpur, IIM Bangalore, IIT Madras and IIIT-Bangalore in addition to short teaching assignments at RUTGERS, USA and AIT, Bangkok. He has wide research interests that include Operations Research, Multi-criteria optimization Decision Theory, Simulation, Enterprise Computing, Programming Languages, Databases, Multimedia and e-Governance. He has authored seven books, several book chapters and papers. He is also a Fellow of IEE (UK) and Computer Society of India. He is a Senior Member of IEEE, ACM and AIS.

Prof. Sadagopan consults widely across different industry segments (Auto, Manufacturing, Banking, IT and Social Sector) and lectures extensively at corporations, industry events and Universities in North America, South America, Europe, Middle East, Asia, Australia and New Zealand on all aspects of IT.

Neelam Sinha (Ph.D., Indian Institute of Science, Bangalore)*neelam.sinha@iiitb.ac.in*

Neelam received her PhD from IISc, Bangalore. Her thesis was on strategies for rapid MR imaging. Her previous stints include MILE Lab, IISc and MR Imaging group at GE Healthcare, Bangalore. Her research interests are in medical imaging and processing.

Srinath Srinivasa (Ph.D., Berlin-Brandenburg Graduate School, Germany)*sri@iiitb.ac.in*

Prof. Srinivasa holds a Ph.D. in Information Systems from the Berlin-Brandenburg Graduate School for Distributed Information Systems, Germany, and an MS from IIT-Madras. He works in the broad areas of web information retrieval, distributed computing, and modelling of non-linear systems. Currently, he is a member of various technical and organizational committees for international conferences, a life member of the Computer Society of India (CSI), and a member of the Board of Studies at Goa University. He is also the recipient of various national and international grants for his research activities.

G Srinivasaraghavan

G. Srinivasaraghavan, PhD is a Partner at Performance Engineering Associates. He has a PhD in Computer Science from the Indian Institute of Technology Kanpur and has over 18 years of industry experience. At Infosys Technologies, India's premier IT firm, he was responsible for the delivery of large, performance-critical IT systems for Fortune 500 clients in the telecom, BFSI and logistics spaces. He has over a dozen published papers in several reputed international fora, including journal of Algorithms, International Journal on Computational Geometry and

Applications and Foundations of Software Technology and Theoretical Computer Science. In his previous position he was Chief Technology Officer at Aztecsoft Ltd(now a part of Mindtree Ltd), where he brought about a radical, product-quality-focussed shift in the firm's approach to quality assessment.

ADJUNCT / VISITING FACULTY

Tridib Roy Chowdhury



Dr. Tridib Roy Chowdhury, graduated with B-Tech, Computer Science, from IIT-Kharagpur in 1986. He then proceeded to complete his M.S. in Computer Science from the University of Maryland, College Park, specializing in large intelligent databases. From 1988-1990, he was the CTO of Micronet Corporation in Maryland, USA and was involved in the development of some of the first desktop based document content management systems. He is currently Director-Technology and Co-Founder of STEX Software, a leading firm in the area of document content management systems and business process management systems. His primary interests are in the area of multi-dimensional indexes, object databases, image compression and quality systems.

Subhajit Datta



Dr. Subhajit Datta received a Ph.D. in computer science from the Florida State University. He has more than 10 years of experience in software design, development, and research at a number of organizations. Subhajit is the author of Software Engineering: Concepts and Applications (Oxford University Press, 2010) and Metrics-Driven Enterprise Software Development (J.Ross Publishing, 2007). More details are available at www.dattas.net.

Rakesh Godhwani



After spending amazing formative years in great organizations in the High-Tech space like Wipro, Intel and Qualcomm, doing different roles in sales, product marketing and business development, Rakesh realized that his passion is to make other's reach their maximum potential and help fulfill their dreams. Rakesh's goal is to create a fun filled learning environment and create effective communicators, and sales/marketing professionals. Rakesh currently heads IIM Bangalore Alumni Association, coaches entrepreneurs in the hi-tech space and is an adjunct faculty at IIM Bangalore, IIM Trichy, IIIT Bangalore and SDMIMD Mysore. Rakesh's first book "Plunngge" was released in July 2011. More details are on www.plunngge.com or www.facebook.com/plunngge

Roland E Hass



Prof. Roland E. Haas studied Computer Science, Mathematics and Electrical Engineering at the University of Karlsruhe and the Technical University of Clausthal, Germany. His studies were supported by the German National Merit Foundation (Studienstiftung des Deutschen Volkes) and the Mercedes-Benz Scholarship Foundation. He received his Doctorate degree from the Technical University of Clausthal in 1997. He was a visiting researcher at the Kyushu Institute of Design, Fukuoka, Japan and at the Daimler Chrysler Research Center in Palo Alto, CA, USA. He has been with Daimler Chrysler for 13 years working in different divisions like Research & Technology, Engineering Passenger Cars, IT Management and Engineering Airbus. From

2001 to 2006 he was Managing Director of Daimler Chrysler Research and Technology India, Bangalore. His relation with IIIT-B dates back to 2002 when he started a lecture series in Automotive IT and Embedded Systems. Having worked on the borderline between information technology and engineering for more than a decade his research broadly focuses on digital product development topics including advanced product data management, engineering workflow automation, concurrent and simultaneous engineering as well as knowledge based engineering. He is also interested in architectural and performance aspects of complex software systems as well as design issues of mechatronical and embedded systems. Recently he started the company QSO Technologies which was incubated by IIITB. QSO Technologies consults clients in R & D outsourcing, develops customized IT solutions in the Life Sciences domain and provides outsourcing services in systems engineering and software development. Roland Haas is the author of numerous papers and 2 books while a third textbook is in the making.

Vivek. K. (Ph.D., Hong Kong Polytechnic University)

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Prof. Vivek obtained his Ph.D. in Computer Science from the Hong Kong Polytechnic University in 2010. Prior to that, he received M.Tech. degree in Electrical Engineering from Indian Institute of Technology Delhi and worked for Motorola India Electronics Ltd, Bangalore for a while. His research interests include digital signal and image processing, pattern recognition and their applications in Biometrics for personal recognition.

Ashwin Mahesh



Dr. Ashwin Mahesh received his PhD degree from University of Washington. He worked as Graduate Assistant at Vanderbilt University, Nashville, Tennessee fall 1991-Spring 1993; Research Assistant, University of Washington, Fall 1993-Summer 1999; Research Scientist, AS & M., Inc./NASA Langley, Hampton Virginia, September 1999 to August 2000, Research Faculty, Goddard Earth Sciences and Technology Center, NASA Goddard Space Flight Center & University of Maryland, September 2000-August 2005. Dr Mahesh is the Co-founder and Editor, India Together, Bangalore. Dr Mahesh also worked as Director - GOIS/Transport, E-Governments Foundation, Bangalore during April 2005-July 2006. He was a Consultant - Second Administrative Reforms Commission, Govt of India, New Delhi during February 2006-December 2006. Dr Mahesh is also a Co-founder and Director, Mapunity, Bangalore since 2006.

Prof. Mahesh has published several papers in both national and international journals.

Shyamala Kamath



Shyamala Kamath started her career in the Public Relations & Hospitality Industry nearly two decades ago. Having done her Travel & Tourism course from Iata-Uftaa (Canada), she worked for thirteen years with M/S Stic Travels (P) Ltd, a General Sales Agent for seven of the World's finest Airlines. She has traveled extensively for various Seminars and training programs run by the various Airlines. Analytical thinking and conflict resolution have been her areas of strength which has given her exposure to many "Train the Trainer" programs.

She has been associated with IIIT-B since 2007. Apart for IIIT-B, she has taught at The Art of Learning Global Entrepreneurship Academy (TALGEA) and Indian Business Academy (IBA).

Muralidhar Koteshwar



Muralidhar Koteshwar has two decades of experience after a B-Tech from IIT Madras, rated as one of the top engineering academies in the world in a career that spans a complete product life-cycle – Product Conceptualization and Development, Usability design, Quality (ISO & SEI CMM), Sales, Marketing, Product management, Competency Development, Corporate Communications and Program management in cross-cultural transoceanic teams.

A part of the team that built India's first Supercomputer the PARAM at CDAC, he subsequently held leadership positions in multinational organisations like Tektronix (color printing division that was taken over by Xerox), Tata Elxsi, Celstream and Trilogy. He is currently the founder and CEO of Usability Consulting India, Director at Parjanya Technologies – an online mentoring platform company and visiting faculty in Usability Engineering at IIIT-B.

Over the last few years he has trained thousands of IT industry professionals in both technical and leadership domains. He is equally passionate about education and as a Member of the Board of Trustees of the Art of Living's Educational Trust was responsible for establishing over 100 educational institutes, employing 1600 staff and catering to 20,000 students. He is the course director for its well known teacher training and parent training programs that have positively impacted over 200000 people worldwide.

He travels worldwide speaking at International Seminars and training thousands of teachers and professionals in schools, homes and organisations.

S. Nagarajan



S. Nagarajan is currently Technical Director at NetApp India, where he contributes to engineering Storage Systems. He has over 28 years of working experience in Computer Systems R & D. His current research interests are in Storage Systems, Emerging Computing Infrastructure, Operating Systems and Computer Architecture. Nagarajan holds a graduate degree in Electrical Engineering from the University of Calicut (topper from the University) and M.S (by Research) from the Indian Institute of Technology, Madras. Previously he was Director (Systems) and Distinguished Technologist at HP, responsible for the R & D activities for Enterprise Servers, Storage & Networking Division. He started his career as R & D systems software engineer at Wipro Ltd., with responsibility for contributing to the development of computer and embedded systems. While at Wipro, he worked on several international assignments with customers such as Intel Corporation, Tandem Computers and Xerox Corporation. He worked as General Manager at Philips R & D, with responsibility for embedded software of Consumer Electronics products. He co-founded Bluefont Technologies, a technology startup in the digital convergence and wireless LAN domain. He also worked as Director of Technology at IPVALUE, contributing to intellectual property valuation. Nagarajan is a Senior Member of the IEEE, Member of the ACM and USENIX.

Joy Prabhakaran



Joy did his B.Tech in E&ECE from IIT Kharagpur (84-88) and MS in EE from Michigan Tech (88-89). At different stages of his career he has been a teacher, an entrepreneur

and has also worked for various companies. Joy has worked in diverse areas of technology and he is passionate about finding synergies between these areas. Lately, his work has been in the areas of mobile technologies, multimedia, printer firmware, aerospace hardware systems and building control technologies. His likes to work at the system conceptualization level and also at the algorithm/logic level. Joy holds two patents granted by the USPTO and also holds a six sigma green belt.

Revathi Shivakumar



Revathi Siva Kumar is a Delhi University (Literature) postgraduate, who has worked for 13 years in The Times of India and Deccan Herald. She handled news and feature supplements and wrote on developmental issues, health, education, environment and social justice. In 2000, she switched to Indiainfo.com as Channel Manager, then joined Infoglobe, an Ohio-based portal on Brain Research, as Team Lead, and next became Special Correspondent for GPS issues at Geospatial Today.

Since 2004, she has been a freelance journalist, writer, poet and instructor. Her projects include a book on rainwater harvesting, six American e-books and scripts for educational CD-Roms. Currently, she is a Senior Editor at Manyagroup. She has taught at Indian Business Academy and Princeton Review, apart from IIIT-B.

Ashok Srinivasan



Ashok Srinivasan obtained his PhD from the University of California at Santa Barbara. He is an Association Professor of Computer Science at Florida State University, and currently visiting IIITB. He has, in the past, worked at IIT Bombay, at the National Center for Supercomputing Applications (NCSA), and at the University of California at Santa Barbara. His research interests are in High Performance Computing applications and algorithms. He works on some of the worlds largest supercomputers and also on accelerators such as GPUs.

Abbas K Sutarwala



Prof. Abbas enjoys developing software and teaching and mentoring good programming. Abbas has over 40 years of hands-on experience in programming and software development with reputed companies like TCS and Tata Elxsi. He has worked as Team-member, Project Leader, Group Leader and on-site Project Manager for several development projects in India and abroad. Abbas was a Senior Faculty at the TCS Corporate Learning Center in Trivandrum and teaches as "Visiting Faculty" at IIIT-B. Abbas authored a book on "Good Programming Skills & Practices" and has delivered workshops on the subject at TCS, Tata-Elxsi, Wipro, CSI, etc.

Abbas is currently a Consultant for Programming Practices at ASL Advanced Systems Ltd., Bangalore.

KRV Raja Subramanian



Dr. Raja Subramanian founded Radix Learning in June 2006 with a team of professionals who share a passion for creating innovative learning solutions and services. Prior to founding Radix Learning, Dr Subramanian co-founded Ascendum

Systems in March 2000 along with a group of professionals hailing from some of the well-known business houses and Ivy-league institutions in India. Before co-founding Ascendum Systems, Dr Subramanian served as Professor of Computer Science and Dean of Distance Learning at BITS Pilani. Dr Subramanian has a unique mix of academic and corporate experience where he has led from the front for over 28 years. Dr Subramanian holds a Master's Degree in Economics, a Master's Degree in Computer Science and a PhD in Computer Science

Gopalaswamy Ramesh



Gopalaswamy Ramesh is an independent consultant and an adjunct professor at IIIT-B and a number of other premier institutions. He has more than 30 years of experience in industry, most of it with Oracle in the Far East, US and India. He was instrumental in setting up Oracle's India Development Center in Bangalore and was the Senior Director of Oracle IDC, growing it from zero to more than 400. He is the author of best selling books Managing Global Software Projects, Software Testing Principles and Practices and Software Maintenance. The first book won the National Best Book Award in India. Two of these books have also been translated to Chinese. His most recent book The ACE of Soft Skills has been used as a prescribed text book for professional development workshops and courses in several institutions including IIT/Madras. He has been a regular speaker and chair / organizer in a number of international conferences. He is a member of the Academic Advisory Group of Project Management Institute (PMI), India and heads the editorial board of the quarterly journal of Software Process Improvement Network (SPIN), Chennai. He consults for a number of companies in India and abroad. His interest areas include Software Engineering, Software Project Management and Soft Skills in the context of global teams. He has taught these subjects in several leading academic institutions including IIIT/Bangalore, IIT/Madras, IIM/Bangalore, Amrita School of Business, XLRI, Jamshedpur and SRM University, Chennai. Ramesh holds a BE in Electronics and Communication Engineering from IISc, Bangalore, MS in Computer Science from IIT/Madras and MS in Engineering Management from Stanford University, USA. He can be reached at gopalaswamy_ramesh@yahoo.com

Dr. Eswar Subramanian (Ph.D, University of Carnegie Mellon)



Dr. Eswaran Subrahmanian received his PhD degree from Carnegie Mellon University. He worked as Trainee Design Engineer, MN Dastur & Company, Consulting Engineers, Calcutta during. He also worked at CMU at various capacities during 1976-1998. He is holding a position as a Research Professor, Institute for Complex Engineered Systems, CMU since 1998. Right now he is working as a Chief Architect/Founder, Elefon Incorporated since 2002.

Dr. Subrahmanian had Consulting Assignments with ABB Corporate Research, Billingstad, Norway, Hosch Research Technology Center, Pittsburg, University Industry Research Co-operation, Boeing Seattle, WA: Technology Consulting, World Bank, Air Products and Lytix. He has published several books and also published several papers in referred journals. He has attended various conferences both in India and abroad.

Dr. Srin Ramaswamy (Ph.D University of Louisiana)



Dr. Ramaswamy earned his Ph.D. degree in Computer Science from the Center for Advanced Computer Studies (CACCS) at the University of Louisiana at Lafayette in 1994. He is a member of the Society for Computer Simulation International, Computing Professionals for Social Responsibility, a Senior member of the IEEE and a Senior member of the Association of Computing Machinery (ACM). He is an active member of IEEE SMCS Technical Committee on Distributed Intelligent Systems and also serves as an Associate Editor for the IEEE Transactions

on Systems, Man and Cybernetics, Part C: Applications and Reviews. In the local community, Dr. Ramaswamy was an active Rotarian in Club 99, the rotary club of Arkansas Little Rock.

Dr. Srinu Ramaswamy is currently leading Industrial Software Systems research at ABB, the Swiss Multinational Fortune 500 company at its India Corporate Research Center (INCRC) in Bangalore, India. His primary responsibility is in research team building and leadership, developing university relationships and engaging in applied research for the creation and execution of projects with transformative value for the company's power technologies and process automation business units. On the academic front, he also serves as a visiting professor at the University of Arkansas at Little Rock and an honorary adjunct professor at the Indian Institute of Information Technology – Bangalore. His research interests are on intelligent and flexible control, behavior modeling, analysis and simulation, empirical software systems research, software stability and scalability; particularly in the design and development of complex software systems. Specific applications include real-time control issues in automation and manufacturing, data mining and distributed real-time applications. His work is motivated by the desire to understand the various requirements to build scalable, intelligent software systems with the inherent ability to successfully respond to observed and reported behavioral changes in their environment.

