

MANDATORY DISCLOSURE

I. NAME OF THE INSTITUTION

SILICON INSTITUTE OF TECHNOLOGY

Silicon Hills, Patia
Bhubaneswar-751024
Orissa
Phone No: (0674) 2725 448, 649, 452
Fax : (0674) 2725 312
e-mail : director@silicon.ac.in
Website : www.silicon.ac.in

II. NAME & ADDRESS OF THE DIRECTOR

Prof (Dr) R K Mishra
Silicon Hills, Patia
Bhubaneswar-751024
Phone : (0674) 2725448
Fax : (0674) 2725312
e-mail : director@silicon.ac.in

III. NAME OF THE AFFILIATING UNIVERSITY

Biju Patnaik University of Technology

IV. GOVERNANCE

❖ Members of the Board and their brief background

The Board of Trustees consists of computer professionals, technocrats and academicians equipped with quality education and global exposure to create an institution, anticipating trend and leading career education into the future. The major strength of Silicon is its vision to build a complete Institute beyond teaching, with value based leadership and an environment of working together to reach the ultimate in students' delightment. The Governing Body consists of Educationists, Technocrats and representatives of the affiliating University and Government. The following are members of the Governing Body.

- | | |
|-------------------------|---------------------------|
| 1. Joe Madiath | Chairman |
| 2. Nitai Gaur Dhall | Vice-Chairman |
| 3. Prof. A.K. Sinha | University Nominee |
| 4. Prof. Ganapati Panda | |
| 5. Prof. P.K. Dash | |
| 6. Sanjeev K Nayak | |
| 7. Dr. Tanmay Panda | |
| 8. G.P. Das | |
| 9. Ramananda Mishra | |
| 10. J.P. Singh | |
| 11. Alok Jena | |
| 12. Prof. R K Mishra | Director/Member Secretary |

❖ Members of Academic Advisory Body

- | | |
|-----------------------------|---|
| 1. Dr. Chitta Baral | Arizona State University, USA |
| 2. Mr. Hrushikesh Mohanty | Central University, Hyderabad |
| 3. Mr. Manoranjan Mahapatra | Hughes Software (India) Ltd |
| 4. Dr. M Palaniswami | The University of Melbourne |
| 5. Dr. R K Ghosh | IIT Kanpur |
| 6. Mr. R Ramaraj | CEO, Satyam Infoway Ltd |
| 7. Dr. Santrupt Mishra | Head Corporate Human Resources,
Aditya Birla Group |
| 8. Mr. R. Narayan | Vice President (Learning &
Development), TCS |
| 9. Mr. Pradip Waychal | Head - Corporate Quality & Delivery
Innovation, Patni Computer Systems Limited |
| 10. Dr. D P Dash | XIMB |
| 11. Mr. Sudip Banerjee | Wipro Technologies |
| 12. Mr. Mohan Tambe | Innomedia Technologies Ltd. |
| 13. Mr. Vinay Deshpande | Encore Software Limited |
| 14. Dr. Sanjib K Panda | National Univ. of Singapore |

- ❖ Frequency of the Board Meetings and Academic Advisory Body
The Governing Body meets twice every year to frame policies and monitor its implementation. The Academics Advisory body meets once every year.

❖ Organizational chart and processes

Annexure – 1

- ❖ Nature and Extent of involvement of faculty and students in academic affairs/improvements
- ❖ Mechanism/Norms & Procedure for democratic/good Governance
- ❖ Student Feedback on Institutional Governance/faculty performance
- ❖ Grievance redressal mechanism for faculty, staff and students

Best Practices

- Orientation programme for newly appointed Faculty members. These are conducted by Senior faculty members of the Institute as well as through resource persons from outside.
- Introduction of Smart cards for staff and students.
- Dress code and other code of conduct (ethics, cleanliness, discipline) for staff and students.
- Online feedback from students on the performance of faculty members.
- Faculty members as mentors to specific group of students.
- Quality circle meetings participated by all staff, held twice every month for improvement of academic as well as social & environmental issues in the campus.
- Orientation programme for new entrants and parents.
- Regular periodic seminars by senior faculty members and outside experts.
- Training students for securing employment.
- Yoga as a compulsory subject for all students.

Strength

- Use of Teaching aids like LCD, OHP and online tests.
- Special classes for academically weak students.

- Chamber consultancy/assistance is available, utilized by students in all subjects.
- A standard guideline to prepare for the lectures, which are adhered to by all faculty members.
- Students do their degree projects in-house under the guidance of faculty members. Many of such projects have been presented in various competitions and won prizes because of the originality of the work.

V. PROGRAMMES

- ❖ Name of the Programmes approved by the AICTE

Computer Science & Engineering	-	120 seats
Information Technology	-	60 seats
Electronics & Telecommunication Engineering	-	120 seats
Electrical & Electronics Engineering	-	120 seats
Applied Electronics & Instrumentation	-	60 seats
MCA	-	60 seats
M.Tech (ENTC)	-	18 seats
M.Tech (CS)	-	18 seats

- ❖ Name of the Programmes accredited by the AICTE

Not yet accredited.

- ❖ For each Programme the following details are to be given:

- Name, Number of seats, Duration, Cut off mark/rank for admission during the last three years, Fee, Placement Facilities, Campus placement in last three years with minimum salary, maximum salary and average salary

Programme	Number of seats (2009)	Duration	Cut off mark/rank for admission during the last 3 years			Fee	Placement (2008)	
			2007	2008	2009		Minimum Salary	Maximum Salary
CS	120	4 years	1301	1733	6124	85000	2.4 lacs	3.1 lac
IT	60	4 years	1850	2693	11425	85000	2.4 lacs	3.1 lac
ET	120	4 years	1162	1280	2119	85000	2.4 lacs	3.2 lacs
EE	120	4 years	1635	1484	2255	85000	2.4 lacs	2.6 lacs
AEI	60	4 years	1675	1827	2945	85000	2.4 lacs	2.6 lacs
MCA	60	3 years	281	181		85000	2.4 lacs	2.6 lacs

- ❖ Name and duration of programme(s) having affiliation/collaboration with Foreign University(s)/Institution(s) and being run in the same Campus along with status of their AICTE approval. If there is foreign collaboration, give the following details:

Details of the Foreign Institution/University:

- Name of the University/Institution
- Address **NOT APPLICABLE**
- Website
- Is the Institution/University Accredited in its Home Country
- Ranking of the Institution/University in the Home Country

- Whether the degree offered is equivalent to an Indian Degree? If yes, the name of the agency which has approved equivalence. If no, implications for students in terms of pursuit of higher studies in India and abroad and job both within and outside the country.
 - Nature of Collaboration
 - Conditions of Collaboration
 - Complete details of payment a student has to make to get the full benefit of collaboration.
- ❖ For each Collaborative/affiliated Programme give the following:
- Programme Focus
 - Number of seats
 - Admission Procedure **NOT APPLICABLE**
 - Fee
 - Placement Facility
 - Placement Records for last three years with minimum salary, maximum salary and average salary
- ❖ Whether the Collaborative Programme is approved by AICTE? If not whether the Domestic/Foreign Institution has applied to AICTE for approval as required under notification no. 37-3/Legal/2005 dated 16th May, 2005
Not Applicable

VI. FACULTY

- ❖ Branch wise list faculty members:

Discipline	Department	Professor	Asst. Prof.	Lecturer	Total
CS	CS	1	4	15	20
IT	IT	0	1	11	12
MCA	MCA	1	2	10	13
ET	ET	1	4	20	25
AEI	AEI	0	1	12	13
EE	EEE	1	4	16	21
BSH	BSH	1	6	25	32
		4	21	110	136

CS -- Comp. Science & Engg.

IT -- Information Technology

ET -- Electronics & Telecommunication

EE -- Electrical & Electronics

AEI-- Applied Electronics & Instrumentation

BSH – Basic Science & Humanities

- Permanent Faculty 136
 - Visiting Faculty
 - Adjunct Faculty
 - Guest Faculty
 - Permanent Faculty: Student Ratio 1 : 15
- ❖ Number of faculty employed and left during the last three years

During the last three years 37 faculty members have joined the institute. During the last three years 8 faculty members have left.

VII. PROFILE OF DIRECTOR/PRINCIPAL WITH QUALIFICATIONS, TOTAL EXPERIENCE, AGE AND DURATION OF EMPLOYMENT AT THE INSTITUTE CONCERNED

1. Name - **Prof. Rati Kant Mishra**
2. Date of Birth : 11.03.1947
3. Educational Qualification : B.Sc.(Engg), M.Tech., Ph.D.
4. Work Experience
 - Teaching 36 years
 - Research
 - Industry
 - Others(Admin) 5 years
5. Area of Specializations : Mechanical Engineering
6. Subjects teaching at Under Graduate Level
Engg. Mechanics, Entrepreneurship, Prod. Mgmt.,
Post Graduate Level : Production Engg.
7. Research guidance
No. of papers published in Masters's
 - National Journals 22
 - Ph.D. - International Journals 2
 - Conferences 8
8. Projects Carried out 3 nos (AICTE)
9. Patents
10. Technology Transfer
11. Research Publications 32
12. No. of Books published with details



VIII. FEE

❖ Details of fee, as approved by State fee Committee, for the Institution.

S.No.	Category	Fixed by the State Fee Committee	Being charged by the Institution
1.	Tuition Fee	85000	85000
2.	University fee (Examination fee, Registration fee etc.)	4500	4500
3.	Hostel fee (Rent etc.)	1200	1200

Time schedule for payment of fee for the entire programme.

First Monday of every Academic Year.

- ❖ No. of Fee waivers granted with amount and name of students - Nil.
- ❖ Number of scholarship offered by the institute, duration and amount – 17 nos per year @ Rs 1000/- per student per month
- ❖ Criteria for fee waivers/scholarship. – Not Applicable
- ❖ Estimated cost of Boarding and Lodging in Hostels.
The Rent for Hostel is Rs 14,400/- per annum and Rs 3600/- towards electricity charges, water and maintenance charges per annum. The Cost of food is calculated on cost-to-cost basis and the meal charges varies from Rs 13/- to Rs 16/- per meal.

IX. ADMISSION

- ❖ Number of seats sanctioned with the year of approval, Number of students admitted under various categories each year in the last three years, Number of applications received during last two years for admission under Management Quota and number admitted.

Courses	2009-2010		2008-2009		2007-2008		2006-2007	
	Sanctioned intake	Actual admissions	Sanctioned intake	Actual admissions	Sanctioned intake	Actual admissions	Sanctioned intake	Actual admissions
CS	120	120	90	90	90	90	60	60
IT	60	60	60	60	60	60	60	60
ET	120	120	120	120	120	120	90	90
EE	120	120	90	90	90	90	90	90
AEI	60	60	60	60	60	60	60	60
MCA	60	60	60	60	60	60		

X. ADMISSION PROCEDURE

- ❖ Mention the admission test being followed, name and address of the Test Agency and its URL (website).
The joint entrance admission test for the State is conducted by Biju Patnaik University of Technology. The website of BPUT is www.bput.org
- ❖ Number of seats allotted to different Test Qualified candidates separately [AIEEE/CET (State conducted test/University tests)/Association conducted test]

<i>Discipline</i>	<i>Orissa-JEE quota</i>	<i>AIEEE</i>	<i>NRI quota</i>	<i>Total seats</i>
AEI	42	9	9	60
CS	84	18	18	120
IT	42	9	9	60
EE	84	18	18	120
ET	84	18	18	120
MCA	51		9	60

- ❖ Calendar for admission against management/vacant seats:
 - Last date for request for applications, Last date for submission of application, Dates for announcing final results.

	Date of Application	Closing date of Application	Declaration of Result

- Release of admission list (main list and waiting list should be announced on the same day)
- Date for acceptance by the candidate (time given should in no case be less than 15 days)
- Last date for closing of admission.
- Starting of the Academic session. *24th August, 2009*
- The waiting list should be activated only on the expiry of date of main list.
- The policy of refund of the fee, in case of withdrawal, should be clearly notified.

XI. CRITERIA AND WEIGHTAGES FOR ADMISSION – ADMISSION IS DONE AS PER THE GUIDELINES OF THE STATE GOVERNMENT

- ❖ Describe each criteria with its respective weightages i.e. Admission Test, marks in qualifying examination etc.
- ❖ Mention the minimum level of acceptance, if any.
- ❖ Mention the cut-off levels of percentage & percentile scores of the candidates in the admission test for the last three years.
- ❖ Display marks scored in Test etc. and in aggregate for all candidates who were admitted.

Item No I - XI must be given in information brochure and must be hosted as fixed content in the website of the Institution.

The Website must be dynamically updated with regard to XII–XV.

XII. APPLICATION FORM

- ❖ Downloadable application form, with online submission possibilities.

XIII. LIST OF APPLICANTS

- ❖ List of candidates whose applications have been received along with percentile/percentage score for each of the qualifying examination in separate categories for open seats. List of candidates who have applied along with percentage and percentile score for Management quota seats. *As per norms of State government.*

XIV. RESULTS OF ADMISSION UNDER MANAGEMENT SEATS/VACANT SEATS
Admissions to management quota of seats are done strictly on merit basis. All the students are made to seat for an interview to gauge their communication skill and basic mathematics level. Then the merit list is prepared.

- ❖ Composition of selection team for admission under Management Quota with the brief profiles of members (This information be made available in the public domain after the admission process is over)
- ❖ Score of the individual candidates admitted arranged in order of merit.
- ❖ List of candidates who have been offered admission.
- ❖ Waiting list of the candidates in order of merit to be operative from the last date of joining of the first list candidates.
- ❖ List of the candidates who joined within the date, vacancy position in each category before operation of waiting list.

XV. INFORMATION ON INFRASTRUCTURE AND OTHER RESOURCES AVAILABLE

LIBRARY:

- Number of Library books/Titles/Journals available (programme-wise)

S.No	Course(s)	Number of titles of the books		Number of volumes		Number of Journals			
		Required	Available	Required	Available	National		International	
						Required	Available	Required	Available
1	Humanities		1092		6484		08		
2	Electronics		1014		4514		28		IEL ONLINE ELSEVIER
3	Electrical		506		3421				
4	Appl. Electronics & Instrun.		42		850				
5	Computer Science & IT & MCA		2013		8101			DELNET	
6	Others		1307		1539				
7	Book Bank		228		15818				
	Total				40727		36		

- List of online National/International Journals subscribed. IEL ONLINE & ELSEVIER
- E-Library facilities *Under progress*

LABORATORY:

For each Laboratory

- List of Major Equipment/Facilities
- List of Experimental Setup

COMPUTING FACILITIES:

- Number and Configuration of Systems 701
- Total number of systems connected by LAN 701

- Total number of systems connected to WAN
- Internet bandwidth 4 mbps / 24 hours
- Major software packages available Oracle 9i, Visual Studio Net, MS-Office-2003, GNU-C, C++, Java, Borland C++, DOS, Redhat LINUX, Window 98, Windows XP, Windows 2003 Server edition
- Special purpose facilities available

WORKSHOP:

- List of facilities available.
 - Games and Sports Facilities Cricket, Volleyball, Table Tennis, Carom
 - Extra Curriculum Activities Music Club, Health Club, Gymnasium, Yoga
 - Soft Skill Development Facilities

Students are trained in the Language Lab to improve their communication skills with the aid of Audio Video facilities. Besides regular classes training is given to each student during their 5th and 6th semester to develop their communication skills, personality development, quantitative techniques, through lectures, practice sessions and group discussion.

Number of Classrooms and size of each 38 nos with av. area 83.13 sq.m.

Number of Tutorial rooms and size of each 9 nos with av. area 45 sq.m.

Number of laboratories and size of each 31 nos with av. area 102.4sq.m.

Number of drawing halls and size of each one with area 225 sq.m.

Number of Computer Centres with capacity of each one with area 3000 sq.m.

Central Examination Facility, Number of rooms and capacity of each. Five 100 seater examination halls with av. Area 190 sq.m.

Teaching Learning process

- Curricula and syllabi for each of the programmes as approved by the University. As prescribed by university.
- Academic Calendar of the University As prescribed by university.
- Academic Time Table
- Teaching Load of each Faculty
 - Two subjects (between 6 to 10 hours classes per week depending on the credit of the course.*
 - Two to three laboratory classes (between 6 to 9 hours per week)*
- Internal Continuous Evaluation System and place
 - According to the academic regulation of the university three internal tests are conducted for every theory subject during a semester in regular interval as specified in the university academic calendar. Every practice class is evaluated out of 10 points.*
- Students' assessment of Faculty, System in place.
 - The institute has a regular practice of taking faculty feedback from the students. The system is online. Questions with graded multiple choice answers are put to the students on different aspects of the faculty*

members including (i) Communication skill, (ii) Behavioural aspects, (iii) Overall effectiveness. The marks and special comments given by the students are disclosed to the concerned faculty members at the end of each semester.

For each Post Graduate programme give the following:

- i. Title of the programme M.Tech (ENTC and Comp.Science)
- ii. Curricula and Syllabi : As per Biju Patnaik University of Technology, Rourkela, Orissa
- iii. Faculty Profile

SI	Name	DESIGNATION	Subject Teaching
1.			
2.			
3.			

- Brief profile of each faculty.
- Laboratory facilities exclusive to the PG programme

Department of Electronics & Telecommunication Engg

SL.No	Name of the Lab	Area	Facilities available
1	Telecommunication system Engg lab-I & II, Design and Simulation Lab,Wireless and Fibre Optics Communication Lab for M.tech		Mat lab,Simulation of various noise,Filter & digital communication signal,Tanner,Spice simulation of various inverters of digital systems,VHDL simulation,satellite communication system,Optical fibre communication system.
2	Project Lab for M.Tech		Dsp starter kit,Evaluation modules,Software development tool code composer studio all,Emulators,OMAP development tools,Ethernet Usb design kit,Microcontroller kit,Microtronics product starter kit with Microtronics firefly module with AlteraEP2C35 firefly PSK development kit,Cyclone III kit with video development board,Lancelot VGA controller IP design kit,DSPACE hardware for real time experiments,CRO 30MHz,Storage Oscilloscope 200 MHz,LCD projector,Free scale code writer for HCS12 microcontroller HCS12 with 5 software licences.

Department of Computer Science

	Name of the Lab	Area	Facilities available
1	Software Tech Lab for M.Tech		P-IV/Duel core/ core 2 duo-20 nos,Network switch,UPS,Server and rational rose software

Special Purpose

Software, all design tools in case: Details as above

Academic Calendar and framework

Post-graduate calendar for the year 2009-2010

2nd Semester M.Tech. (Electronics & Telecommunication)

Classes started on 16th July, 2009

1st Internal Examination: 1st week of September, 2009

2nd Internal Examination : 2nd week of October, 2009

3rd Internal Examination: 3rd week of November, 2009

1st Semester M.Tech. Electronics & Telecommunication, and Computer Science & Engg.

Classes will start on 1st week of October, 2009

1st Internal Examination : 2nd week of November, 2009

2nd Internal Examination : 2nd week of December, 2009

3rd Internal Examination : 3rd week of January, 2010

Research focus & industry linkage

List of typical research projects

Design and Development of Flexible of DSP and FPGA based Flexible AC Transmission line Relay : Approved by Power Research Industrial, Bangalore

Cost: Approximately 23.0 lakhs

A new research project on "Islanding and Protection of Distribution Generator connected Microgrid will be submitted to AICTE for funding.

M.Tech. research project on Electronics & Telecommunication will be taken up in January, 2010.

Ph.D. Projects:

Electronics & Telecommunication Engineering Department.

System Identification, Adaptive filters and parameter estimation, H- ∞ filters for tracking, Biomedical signal processing, Hardware development in VLSI & FPGA systems.

4 Faculty members are working towards their Ph.D. degree.

One Ph.D. thesis in the Computer Science area will be submitted very soon.

Publications:

Yet to come out of Master projects.

Placement :

One M.Tech. student has joined the Silicon Institute of Technology, Bhubaneswar.

Admission Procedure:

An online test is conducted by the Biju Patnaik University of Technology for admission to Masters Program. The number of seats approved by AICTE during 2009-2010 is 18 for both Electronics & Telecommunication and Computer Science & Engineering.

Fee structure :

Rs.85000/- per year per M.Tech. student.

Hostel facilities: Available.

Contact address of coordinators of PG programs:

1. Mr. Badri Narayan Sahu,
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NOTE: **Suppression and/or misrepresentation of information would attract appropriate penal action.**